The 3.2 Gpix LSST camera will be read out by means of 189 highly segmented 4k x 4k CCDs. A total of 3024 video channels will be processed by a modular, in-cryostat electronics package based on two custom multichannel analog ASICs now in development. Performance goals of 5 electrons noise, 0.1% electronic crosstalk, and 80 mW power dissipation per channel are targeted. The focal plane is organized as a set of 12k x 12k sub-mosaics (“rafts”) with front end electronics housed in an enclosure falling within the footprint of the CCDs making up the raft. CCD surfaces within a raft are required to be coplanar to within 6.5 microns. The assembly of CCDs, base-plate, electronics boards, and cooling components constitutes a self-contained and testable 144 Mpix imager (“raft tower”), and 21 identical raft towers make up the LSST science focal plane. Electronic, mechanical, and thermal prototypes are now undergoing testing and results will be presented at the meeting.